

METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE
HAVING....

Devendra Kumar

Appl. No.: Unknown Atty Docket: ASMJP.118AUS

Fig. 1a

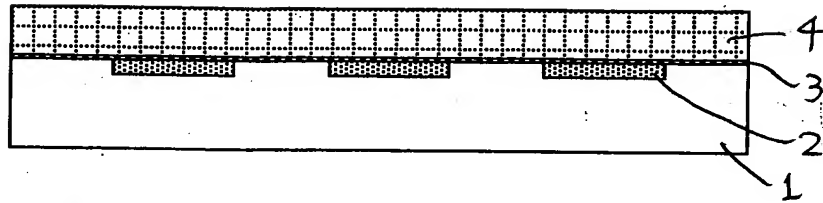


Fig. 1b

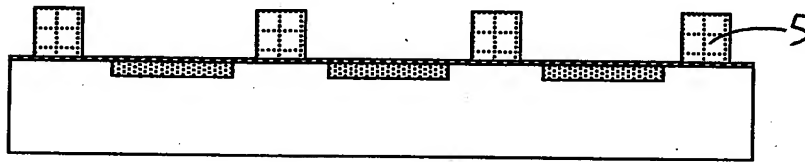


Fig. 1c

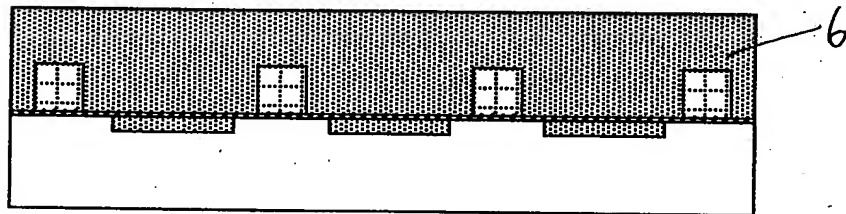
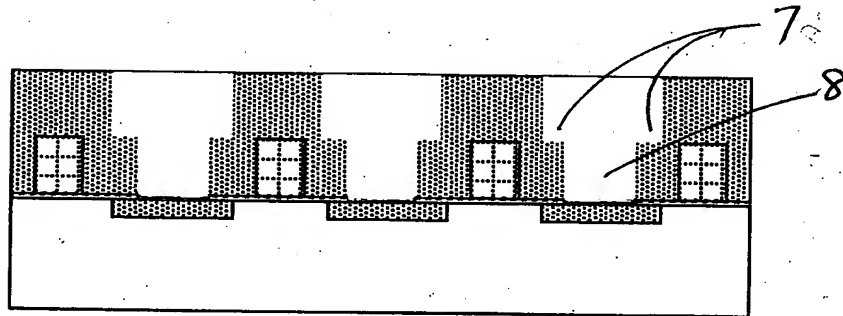


Fig. 1d



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Fig. 1e

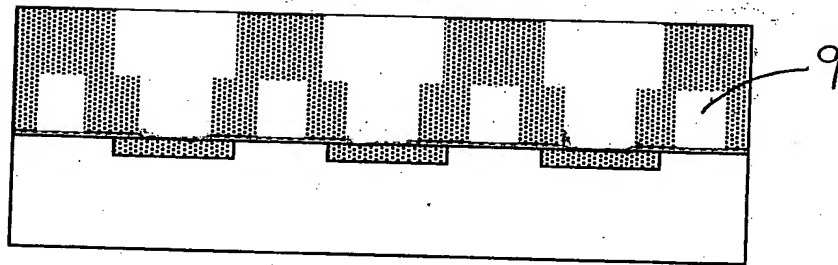
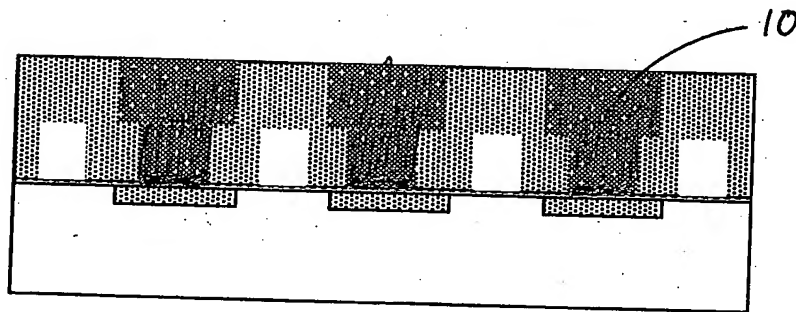


Fig. 1f



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Fig. 2a

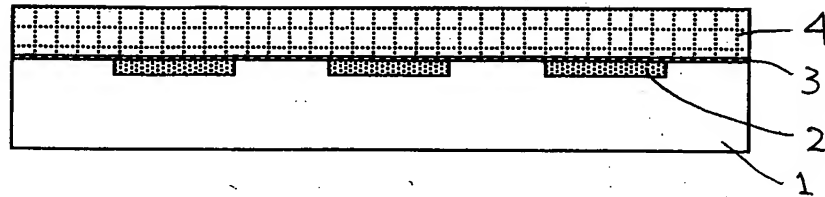


Fig. 2b

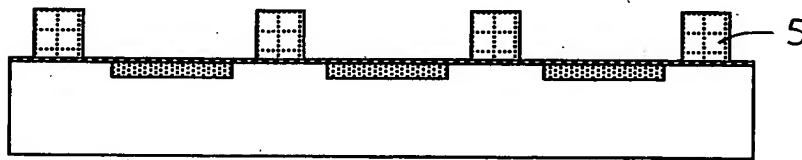


Fig. 2c

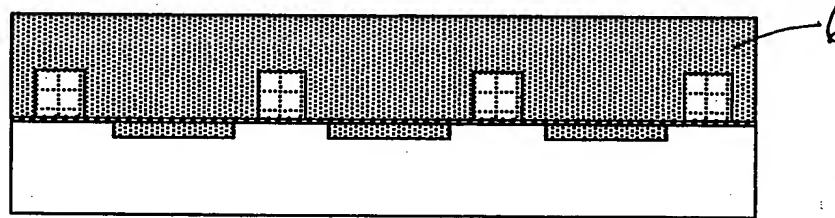
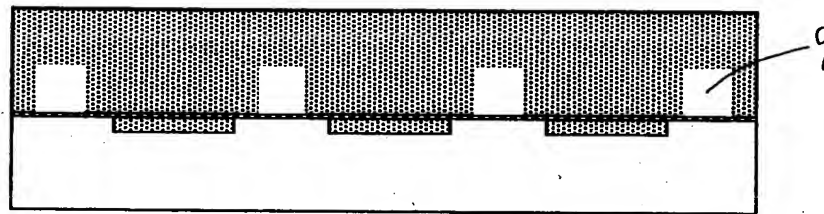


Fig. 2d



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Fig. 2e

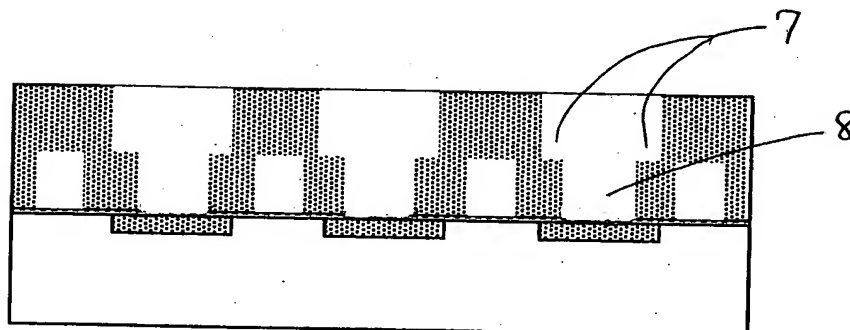
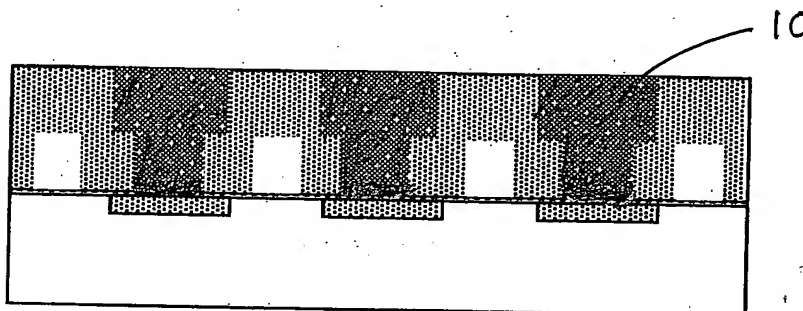


Fig. 2f



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Fig. 3a

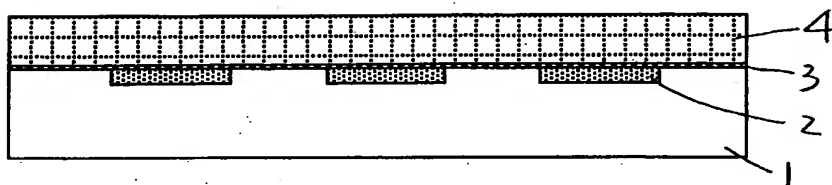


Fig. 3b

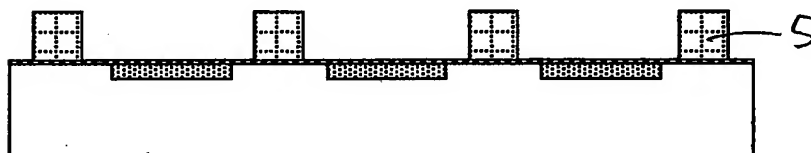


Fig. 3c

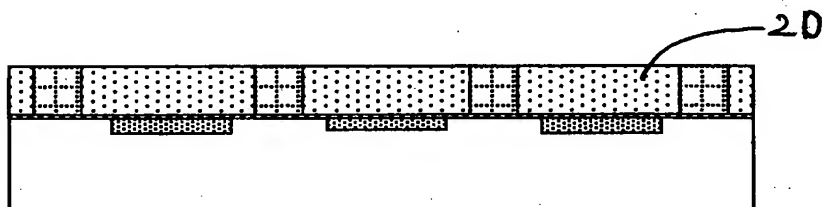
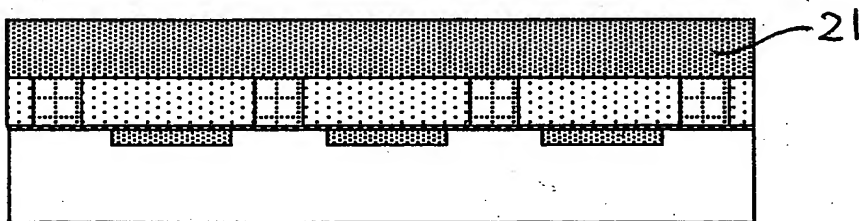


Fig. 3d



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Fig 3e

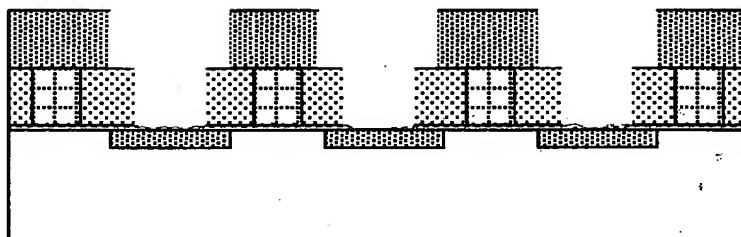


Fig.3f

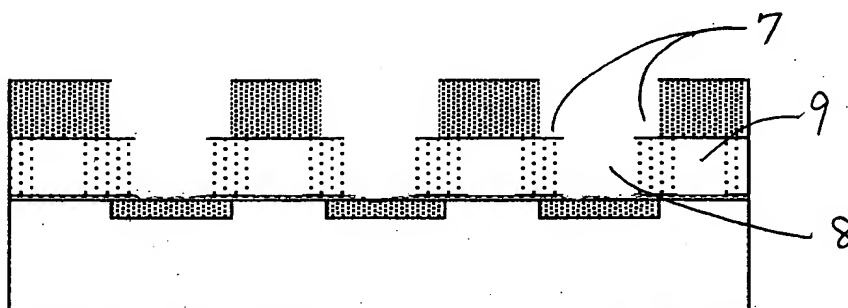


Fig.3g

